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**Paul Murphy about UltraSPARC T2plus**

As usual Paul brings an interesting perspective into discussion:On the other hand.. the way the processors are coupled - done by replacing the the T2's on board 10Gbyte facility - demonstrated that Sun can now produce highly customized versions of the core CPU set and suggests what I believe may be a unique performance opportunity for this product line.Taking into consideration the additional transistor budget by the usage of  upcoming process technologies, it should be feasible to integrate other interconnect technologies as well, for example Infiniband on die (just think about the latency advantages of an Infiniband port directly conneted to the crossbar) or the integration of additional support circuits for special tasks. I assume, there is only one limit ... the pin count of the proc ... at a certain point you can´t get all the interfaces out of the chip in an economical feasible manner.

Posted by Joerg Moellenkamp in English, Oracle at 12:24