

Thursday, February 15, 2007

### **Niagara II at ISSCC 2007**

At OpenSparc .net you will find a in-depth presentation of the upcoming Niagara II processor: "An 8-core, 64-thread, 64-bit, power efficient SPARC SoC (Niagara2)". The presentation was held at the International Solid State Circuits Conference 2007.

Posted by Joerg Moellenkamp in English, Oracle at 06:38