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HT-enabled opteron-socket compatible coprocessor

A really neat concept: Plugging a FPGA-Coprocessor right into an Opteron socket. The idea that came into my mind, was a Niagara with an opteron compatible interface as a web/transaction accelerator. Or the opteron for accelerating a Niagara on certain task. Mix and match as you need it. Running on multiple operating systems coupled with RDMAoverHypertransport for communcation. This would only the same idea like the one of DRC taken to an extreme.

Posted by Joerg Moellenkamp in [Braindump](#), English at 15:30