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**Live from CeBIT: Link to Anandtech review of UltraSPARC T1**

Here is a nice review of the T1 Processor.

There are certainly many ways to tackle computing problems, and it's good to see some new approaches other than the standard "more cache" and "higher clock speeds" that are so common.

For all people that cannot visit me on CeBIT, it's a good introduction to UltraSPARC T1. It even use much material of our presentation.

Posted by Joerg Moellenkamp in English, Oracle at 17:07